

APPLICATION DATA SHEET

Electronic Version 0.0.11

Stylesheet Version: 1.0

Attorney Docket Number: 030712-36

Publication Filing Type: new-utility

Application Type: utility

Title of Invention: APPARATUS AND METHOD FOR DESIGNING SEMICONDUCTOR
INTEGRATED CIRCUIT

Legal Representative:

Attorney or Agent: Donald R. Studebaker

Registration Number: 32815

Customer Number Correspondence Address: 22204

22204

Foreign Priority:

2003-399900 JP 2003-11-28 Priority Claimed

Assignee (Publish): Oki Electric Industry Co., Ltd.

7-12, Toranomon 1-chome,

Minato-ku, Tokyo JP

INVENTOR(s):

Primary Citizenship: JAPAN

Given Name: Hiroki

Family Name: GOKO

Residence City: Tokyo

Residence Country: JP

Address: c/o Oki Electric Industry Co., Ltd.

7-12, Toranomon 1-chome,

Minato-ku, Tokyo, JP

Primary Citizenship: JAPAN

Given Name: Junichi

Family Name: TAMURA

Residence City: Tokyo
Residence Country: JP
Address: c/o Oki Electric Industry Co., Ltd.
7-12, Toranomon 1-chome,
Minato-ku, Tokyo, JP